IBM Docket: BUR920040001US1

Declaration and Power of Attorney for Patent Application

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name; I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled: A SYSTEM AND METHOD OF PROVIDING ERROR DETECTION AND CORRECTION CAPABILITY IN AN INTEGRATED CIRCUIT USING REDUNDANT LOGIC CELLS OF AN **EMBEDDED FPGA**

the regulfication o	f which (chack one)			
the specification o	f which (check one)			
X	is attached hereto.			
	was filed on		as Application Serial No	and was amended on
I hereby state tha amended by any ar	t I have reviewed and mendment referred to a	understand the con above.	tents of the above- identified specific	cation, including the claims, as
I acknowledge the Code of Federal Re		ation which is mater	ial to the patentability of this applicat	ion in accordance with Title 37
certificate listed be	ign priority benefits un elow and have also ider application on which p	ntified below any fore	States Code, §119 of any foreign applice eign application for patent or inventor	cation(s) for patent or inventor's 's certificate having a filing date
Prior For	eign Application(s):			
Number	c	ountry	Day/Month/Year	Priority Claimed
subject matter of e the first paragraph of this application	ach of the claims of this of Title 35, United Stat	s application is not di es Code, §112, I ackr Code of Federal Re	120 of any United States application(s) sclosed in the prior United States applinowledge the duty to disclose informatingulations, §1.56 which occurred betworf this application:	ication in the manner provided by ion material to the patentability
Prior U.S	. Applications:			
Serial No).	Filing Date		Status
helief are believed	to be true; and further unishable by fine or imp	that these statemen orisonment, or both,	knowledge are true and that all stater its were made with the knowledge that under Section 1001 of Title 18 of the U	: willful false statements and the Inited States Code and that such

willful false statements may jeopardize the validity of the application or any patent issued thereon.

As a named inventor, I hereby appoint the following attorneys and/or agents to prosecute this application and transact all business in the Patent and Trademark Office connected therewith: Anthony Canale, Reg. No. 51,526; Mark F. Chadurjian (Reg. No. 30,739); Richard A. Henkler, Reg. No. 39,220; Richard M. Kotulak (Reg. No. 27,712); Michael LeStrange (Reg. No. 53,207); William D. Sabo (Reg. No. 27,465); Robert A. Walsh (Reg. No. 26,516); Christopher A. Hughes (Reg. No. 26,914); John E. Hoel (Reg. No. 26,279); Joseph C. Redmond, Jr. (Reg. No. 18,753); Steven Fischman (Reg. No. 34,594); Leopold Presser, (Reg. No. 19,827); Frank S. DiGiglio (Reg. No. 31,346); John S. Sensny (Reg. No. 28,757); William C. Roch (Reg. No. 24,972); Leslie S. Szivos (Reg. No. 39,394) and Edward W. Grolz (Reg. No. 33,705).

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